**Nemónicos de mi procesador**

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| # de Bits | 1 | 3 | 6 | 6 | 6 | 1 | 1 | 1 | 16 | 1 | 2 | 16 | 1 | 2 |
| Nem | CmpAL | F | @D | @A | @B | MX1 | RW | Reset | IMMED | Rb/IM | Cond | Tkn | OUT | FIN |
| INSTRUCCIONES I/O DEL PROCESADOR | | | | | | | | | | | | |  |  |
| IN [R1] [DATAIN] | X | X | R1 | X | X | 0 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| OUT [R1] | X | X | X | X | R1 | 0 | 0 | 0 | X | 0 | 00 | X | 1 | 00 |
| RESETFILEREG | X | X | X | X | X | 0 | X | 1 | X | 0 | 00 | X | 0 | 00 |
| INSTRUCCIONES DE LA UNIDAD ARITMÉTICA | | | | | | | | | | | | |  |  |
| ADDI [R3] [R1] [R2] | 0 | 100 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| SUB [R3] [R1] [R2] | 0 | 101 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| ANDI [R3] [R1] [R2] | 0 | 000 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| XOR [R3] [R1] [R2] | 0 | 010 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| NOT [R2] [R1] | 0 | 011 | R2 | R1 | X | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| SHA [R3] [R2] [R1] | 0 | 110 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| SHL [R3] [R1] [R2] | 0 | 111 | R3 | R1 | R2 | 1 | 1 | 0 | X | 0 | 00 | X | 0 | 00 |
| INSTRUCCIONES DE LA UNIDAD DE COMPARACIÓN | | | | | | | | | | | | |  |  |
| CMPLT [R1] [R2] JL [I] | 1 | 000 | X | R1 | R2 | 0 | 0 | 0 | X | 0 | 01 | I | 0 | 00 |
| CMPLTU [R1] [R2] JL [I] | 1 | 100 | X | R1 | R2 | 0 | 0 | 0 | X | 0 | 01 | I | 0 | 00 |
| CMPLEQ [R1] [R2] JL [I] | 1 | 001 | X | R1 | R2 | 0 | 0 | 0 | X | 0 | 01 | I | 0 | 00 |
| CMPLEQU [R1] [R2] JL [I] | 1 | 101 | X | R1 | R2 | 0 | 0 | 0 | X | 0 | 01 | I | 0 | 00 |
| JA [I] | X | X | X | X | X | 0 | 0 | 0 | X | 0 | 11 | I | 0 | 00 |
| FIN | X | X | X | X | X | 0 | 0 | 0 | X | X | 11 | [ACT] | 0 | 01 |
| FATALERROR | X | X | X | X | X | 0 | 0 | 0 | X | X | 11 | [ACT] | 0 | 10 |
| CTRLERROR | X | X | X | X | X | 0 | 0 | 0 | X | X | 00 | X | 0 | 11 |